

## AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An apparatus, comprising:  
a buffer having a trigger, ~~integrated on the buffer coupled with~~ a component further coupled with a simultaneous bi-directional (SBD) memory bus having ternary logic levels, the trigger ~~is to facilitate~~ one or more of un- intrusively observing, reading, and echoing of one or more of a plurality of signals transmitted on the SBD memory bus, wherein the trigger ~~operates~~ to instruct the buffer ~~using via~~ one or more of a control signal-based indication, an address signal-based indication, and a time-based indication;  
and  
a diagnostic device coupled with the buffer, the diagnostic device to facilitate one or more of detecting, accessing, and reading of the plurality of echoed signals; and  
an observability port coupled with the buffer, the observability port to receive the plurality of echoed signals, wherein the observability port includes a logic observability port.
- 2-5. (Cancelled)
6. (Currently Amended) A method, comprising:  
transmitting a plurality of signals on a simultaneous bi-directional (SBD) memory bus having ternary logic levels;  
~~a buffer having a trigger, integrated on a component coupled with the bus, un-~~  
intrusively facilitating observing, reading and and/or echoing of one or more of a the plurality of signals transmitted on the SBD memory bus via  
a trigger, the SBD bus coupled with a component further coupled with a

buffer having the trigger, wherein the trigger ~~operates to~~ instruct the buffer ~~using via~~ one or more of a control signal-based indication, an address signal-based indication, and a time-based indication; ~~and~~  
~~a diagnostic device coupled with the buffer, the diagnostic device facilitating one or more of~~ detecting, accessing, ~~and and/or~~ reading of the plurality of echoed signals via a diagnostic device, the diagnostic device coupled with the buffer; and  
receiving the plurality of echoed signals via an observability port, wherein the observability port includes a logic observability port.

7-13. (Cancelled)

14. (Currently Amended) A system, comprising:

a memory coupled with a microprocessor;

~~an input/output (I/O) port~~;

~~a~~ the microprocessor coupled with a buffer having a trigger;

~~a~~ the buffer, having a trigger, integrated on coupled with a component further

coupled with a simultaneous bi-directional (SBD) memory bus having

ternary logic levels, the trigger ~~is to~~ facilitate one or more of un-

intrusively observing, reading, and echoing of a plurality of signals

transmitted on the SBD memory bus, wherein the trigger ~~operates to~~

instruct the buffer ~~using via~~ one or more of a control signal-based

indication, an address signal-based indication, and a time-based indication;

~~and~~

a diagnostic device coupled with the buffer, the diagnostic device to facilitate one or more of detecting, accessing, and reading of the plurality of echoed signals; and  
an observability port coupled with the buffer, the observability port to receive the plurality of echoed signals, wherein the observability port includes a logic observability port.

15-22. (Cancelled)

23. (New) The system of claim 14, wherein the diagnostic device comprises one or more of a logic analyzer and a bus analyzer, the diagnostic device coupled to an observability bus, the observability bus further coupled to the observability port.

24. (New) The system of claim 14, wherein the plurality of signals are communicated via a wireless communication.

25. (New) The system of claim 14, wherein the plurality of echo signals comprise frequencies between a minimum frequency of 5 GHz and a maximum frequency of 500 GHz.

26. (New) The apparatus of claim 1, wherein the diagnostic device comprises one or more of a logic analyzer and a bus analyzer, the diagnostic device coupled to an observability bus, the observability bus further coupled to the observability port.

27. (New) The apparatus of claim 1, wherein the plurality of signals are communicated via a wireless communication.

28. (New) The apparatus of claim 1, wherein the plurality of echo signals comprise frequencies between a minimum frequency of 5 GHz and a maximum frequency of 500 GHz.

29. (New) The method of claim 6, wherein the diagnostic device comprises one or

more of a logic analyzer and a bus analyzer, the diagnostic device coupled to an observability bus, the observability bus further coupled to the observability port.

30. (New) The method of claim 6, further comprising communicating the plurality of signals via a wireless communication.

31. (New) The method of claim 6, wherein the plurality of echo signals comprise frequencies between a minimum frequency of 5 GHz and a maximum frequency of 500 GHz.

32. (New) A machine readable medium having stored thereon data representing sets of instructions which, when executed by a machine, cause the machine to:

transmit a plurality of signals on a simultaneous bi-directional (SBD) memory bus

having ternary logic levels;

un-intrusively observe, read and/or echo of one or more of the plurality of signals

transmitted on the SBD memory bus via a trigger, the SBD bus coupled

with a component further coupled with a buffer having the trigger,

wherein the trigger to instruct the buffer via one or more of a control

signal-based indication, an address signal-based indication, and a time-

based indication;

detect, access, and/or read of the plurality of echoed signals via a diagnostic

device, the diagnostic device coupled with the buffer; and

receive the plurality of echoed signals via an observability port, wherein the

observability port includes a logic observability port.

33. (New) The machine readable medium of claim 32, wherein the diagnostic device comprises one or more of a logic analyzer and a bus analyzer, the diagnostic device coupled to an observability bus, the observability bus further coupled to the observability

port.

34. (New) The machine readable medium of claim 32, wherein the sets of instruction which, when executed, further cause the machine to communicate the plurality of signals via a wireless communication.

35. (New) The machine readable medium of claim 32, wherein the plurality of echo signals comprise frequencies between a minimum frequency of 5 GHz and a maximum frequency of 500 GHz.